IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Taylor et al. I hereby certify that this paper is being filed electronically with the U.S. Patent and Trademark Office U.S. Serial No. : 10/829,495 on this date: Filed : April 22, 2004 Title : "Method and System for Fail-Safe Renaming of Logical Circuit Identifiers for January 29, 2009 Rerouted Logical Circuits in a Data Network" /Felipe Hernandez/ Art Unit : 2419 Felipe Hernandez Registration No. 61,971 Examiner : Ashley L. Shivers Attorney for Applicants

RESPONSE TO THE NON-FINAL OFFICE ACTION DATED OCTOBER 29, 2008

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the non-final action date October 29, 2008, please enter the following amendments and consider the accompanying remarks.

Amendments to the specification begin on page 2 of this paper.

Amendments to the claims begin on page 3 of this paper.

Remarks begin on page 8 of this paper.